

Sriram Sridharan

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Education

PhD, Electrical Engineering, May 2009

The University of Texas at Austin

Overall GPA: 3.91/ 4.00

Major GPA: 3.95/ 4.00

Master of Science, Electrical Engineering, August 2006

The University of Texas at Austin

Overall GPA: 3.90/ 4.00

Major GPA: 3.95/ 4.00

Bachelor of Engineering, Electrical Engineering, May 2004

Indian Institute of Technology Madras

Overall GPA: 9.12/ 10.00

Major GPA: 9.42/ 10.00

Related Courses

Information Theory, Algorithms – Techniques and Theory, Algorithms for Computational Biology, Advanced Telecommunication Networks, Advanced Wireless Space Time Communications, Channel Coding, System Theory, Communication Networks – Analysis and Design, Graduate Real Analysis, Graduate Probability Theory, Digital Communications, Probability and Stochastic Processes, Wireless Communications, Analog Communications, Adaptive Signal Processing, Digital Signal Processing, Algebraic Structures

Experience

January 2006 – Present

Research Assistant, Wireless, Networks and Communications Group, University of Texas Austin

Research Assistant under Dr. Sriram Vishwanath in the field of Information Theory and Bio-Informatics.

Sep 04 – Dec 05

Teaching Assistant, University of Texas Austin

Teaching Assistant for the lab component of Introduction to Electrical and Computer engineering in Fall 2004 and 2005, and Introduction to programming in Spring 2005.

Aug – Nov 2003

Teaching Assistant, Indian Institute of Technology Madras

Teaching assistant for Measurements and Instrumentation lab. Was in charge of the lab for the experiments involving A/D converters.

May – July 2003

Summer Internship Trainee, Race Technologies

Worked on Building Management System (BMS). Underwent training on integration of controls in buildings.

Academic Experience

Information Theory : Multiple Access Channels

Working on deriving sufficient conditions for the optimality of source–channel separation in a network setting. Also working on rate distortion theory for a Gaussian multiple access channel with correlated sources and with more than one receiver.

Project on Bio-Informatics

Implemented the Universal Denoising Algorithm and tested it for text denoising. Currently using it for denoising Biological data like DNA sequences and Microarray data.

Project on Error Control Coding, December 2004

Involved in the implementation of error correcting codes using Labview.

Project Associate, Video coding using H.264, January 2004 to June 2004

Implemented the H.264 Baseline profile video coding encoder using C. Implemented the features of Intra–frame and Inter–frame prediction and coding. Coordinated the integration of the video encoder and decoder

Project member, Wave Energy Conversion, May 2002 to July 2002

Built a simulation model for a wave energy plant using Matlab (Simulink). Implemented a resistor control circuit to increase the efficiency of the plant.

Skills

Proficient with programming languages C and C++
Software – Matlab, Labview, Scilab, Spice, Orcad
Platforms – Windows 98, XP, Linux

Accomplishments

Ranked 3rd in the Department of Electrical Engineering at the Indian Institute of Technology Madras out of a total of 110 students (July 2004).
Won silver medal for highest CGPA in the 3rd year of undergraduate study.
Won silver medal in the International Chemistry Olympiad held in Copenhagen, Denmark in July 2000. Was one of four selected in the Indian team out of 15000 applicants.
Won the National Talent Search Scholarship from 1998 – 2004.

Publications

S. Bodas, J. Grubb, S. Sridharan, T. Ho and S. Vishwanath, "Network with Costs : Timing and Flow Decomposition", Submitted to WNC3 WiOpt, 2007.
S. Sridharan, W. Wu and S. Vishwanath, "Enabling Source Channel Separation for Communication Networks – The Ulink Case," Milcom, Oct. 2006

Employability Status: **Student Visa**