

# Shreeshankar Bodas

284 Harvard St # 66  
Cambridge, MA 02139

shreeshankar@mail.utexas.edu

1-512-296-7117

<https://webspace.utexas.edu/srb568/www/>

## Education

PhD Candidate, The University of Texas at Austin (Expected: December 2010)

### **M.S., Electrical and Computer Engineering, December 2007**

The University of Texas at Austin

Overall GPA = 4.00/4.00

### **B. Tech., Electrical Engineering, July 2005**

{Minor: Operations Research}

IIT Madras, India

Overall GPA = 9.47/10.00, Department Rank = 3/120

### **Graduate Coursework**

Digital Communications	Convex Optimization	Communication Networks	Information Theory
Nonlinear Programming	Functional Analysis	Theory of Probability (I, II)	Algorithms
System Theory	Stochastic Control Theory	Real Analysis	

PhD Dissertation: “High-performance Scheduling Algorithms for Wireless Networks” (Fall 2010)

Masters Thesis: “Flow Decomposition of Cost-Constrained Networks” (Fall 2007)

## Experience

### **Graduate Research Assistant (Summer 2006 – )**

I work as a GRA in the ECE Dept., UT Austin. My research focuses on the development and mathematical analysis of scheduling algorithms for wireless networks.

### **Teaching Assistant (Fall 2005, Spring 2006, Fall 2009)**

## Internships

### **Summer Intern, Samsung Telecommunications America (June – August, 2010)**

Development of power-control and scheduling algorithms for a femtocell-based cellular network.

### **Interim Engineering Intern, Qualcomm, Inc. (May–August, 2008)**

Development of scheduling algorithms for a femtocell-based cellular network.

### **Summer Intern, Wavelet Groups Pvt. Ltd., India (May–June, 2004)**

Development of a signal-processing software in C.

### **Summer Intern, Bit Mapper Pvt. Ltd., India (May–July, 2003)**

Implementation of a co-ordinate conversion algorithm in VLSI, using VHDL.

## Publications

Low-complexity Scheduling Algorithms for Multi-channel Downlink Wireless Networks, *S. Bodas, S. Shakkottai, L.Ying, R. Srikant*; IEEE Infocom Conf., March 2010.

On Scheduling over Multi-server Wireless Systems, *S. Bodas, S. Shakkottai, L.Ying, R. Srikant*; Information Theory and Applications (ITA) Workshop, February 2010.

Scheduling in Multi-Channel Wireless Networks: Rate Function Optimality in the Small-Buffer Regime, *S. Bodas, S. Shakkottai, L.Ying, R. Srikant*; ACM SIGMETRICS/Performance Conf., June 2009.

Communication Through Jamming over a Slotted ALOHA Channel, *S. Bhadra, S. Bodas, S. Shakkottai, S. Vishwanath*; IEEE Transactions on Information Theory, November 2008.

Random Access Over Multiple Access Channels: A Queuing Perspective, *S. Bodas, S. Vishwanath, V. Subramanian*; 42nd Annual Conference on Information Sciences and Systems (CISS), March 2008.

Expressive Analytical Model for Routing Protocols in Mobile Ad Hoc Networks, *T. Jun, A. Dalton, S. Bodas, C. Julien, S. Vishwanath*; IEEE International Conference on Communications (ICC), May 2008.

Network with Costs: Timing and Flow Decomposition, *S. Bodas, J. Grubb, S. Sridharan, T. Ho, S. Vishwanath*; WiOpt Conf. (WNC3 workshop), April 2007.

## Talks

Scheduling for Small Delay in Multi-channel Wireless Networks, Seminar Series, Texas A&M University, March 2010.

Scheduling for Small Delay in Multi-Channel Wireless Networks, *S. Bodas, S. Shakkottai, L. Ying, R. Srikant*; Information Theory and Applications (ITA) Workshop - Graduation Day Talk, February 2010.

## Posters

The WNCG Open-house, UT Austin 2008, 2009, 2010

The Winedale Workshop, Winedale, Texas 2008, 2009

The Texas Wireless Symposium, Austin, Texas 2008, 2009

## Honors and Awards

The WNCG Student Leadership Award (2010)

The National Talent Search (NTS) Scholarship of the Indian Government (1999)

## Skill-set

Proficient in C, MATLAB,  $\LaTeX$   
Worked with VHDL, Perl, ModelSim

**Employability Status:** Student Visa (F-1)